

	Hits	Search Text	DBs	Time Stamp
1	6	((("4,853,250") or ("6,153,852") or ("6,231,725")).PN. or ((2002/0029747) or (2003/0097988) or (2003/0111963)).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/03/08 09:48
2	12	((("4,853,250") or ("6,153,852") or ("6,231,725") or ("20020029747") or ("20030097988") or ("20030111963")).PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/03/08 13:32
3	11	(ETP and ICP)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/03/08 14:15
4	2	("6184585").PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/03/10 12:08
5	0	((("electroluminescent and (IC (integrated adj circuit adj chip)))").PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/03/10 12:13
6	1789	(electroluminescent and (IC (integrated adj circuit adj chip)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/03/10 13:13
7	172	S105 and interconnect	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/03/10 13:07
8	2903	315/169.3,156,159,160,167.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/03/10 13:08
9	76	S108 and interconnect	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/03/10 13:11
10	8298	257/666,668,672,690-692.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/03/10 13:11
11	1113	S110 and interconnect	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/03/10 13:12
12	457	S111 and terminal and substrate	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/03/10 13:12
13	1	S112 and (electroluminescent and (IC (integrated adj circuit adj chip)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/03/10 13:22
14	8298	257/666,668,672,690-692.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/03/10 13:53
15	1113	S115 and interconnect	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/03/10 13:53
16	457	S116 and terminal and substrate	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/03/11 10:28
17	156	345/58.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/03/11 09:47
18	67	S119 and substrate	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/03/11 09:47
19	2340	345/204-206.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/03/11 09:47
20	979	S121 and substrate	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/03/11 09:47
21	43	S123 and interconnect and terminal	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/03/11 09:48
22	8298	257/666,668,672,690-692.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/03/11 10:27
23	1113	S125 and interconnect	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/03/11 10:27
24	0	S126 and terminal and substrate and computer	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/03/11 10:28
25	132	S126 and terminal and substrate and computer	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/03/11 10:29
26	0	S126 and terminal and substrate and computer and IC and electroluminescent	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/03/11 10:29

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27	56	S126 and terminal and substrate and computer and IC	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/03/11 10:29

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1	substrate and el and @ad<"20020830" and pad and insulat\$4 and (power adj supply) same (line connect\$4 interconnect\$4)
2	substrate and el and @ad<"20020830" and pad and insulat\$4 and (power adj supply) same (line connect\$4 interconnect\$4)
3	substrate and el and @ad<"20020830" and pad and insulat\$4 and (power adj supply) same (line connect\$4 interconnect\$4) and (ic integrated adj circuit chip)
4	substrate and el and @ad<"20020830" and pad and insulat\$4 and (power with supply with (line traceconnect\$4 interconnect\$4)) and (ic integrated adj circuit chip)
5	substrate and el and @ad<"20020830" and pad and insulat\$4 and (power with supply with (line trace connect\$4 interconnect\$4)) and (ic integrated adj circuit chip) and substrate with (line trace connect\$4 interconnect\$4)
6	substrate and el and @ad<"20020830" and pad and insulat\$4 and (power with supply with (line trace connect\$4 interconnect\$4)) and (ic integrated adj circuit chip) and substrate with (line trace connect\$5 interconnect\$5) same power adj suppl\$5
7	substrate and el and @ad<"20020830" and pad and insulat\$4 and (power with supply with (line trace connect\$4 interconnect\$4)) and (ic integrated adj circuit chip) and substrate with (line trace connect\$5 interconnect\$5) same power adj suppl\$5 and (switch\$4 cross\$4 matri\$6) same (connect\$5 interconnect\$5)
8	substrate and (el elctrolumines\$6) and @ad<"20020830" and flexible near4 substrate and (switch\$4 cross\$4 matri\$6) same (connect\$5 interconnect\$5 trace line conduct\$6) and (insulat\$5 isolat\$5) with (connect\$5 interconnect\$5 trace line conduct\$6)
9	substrate and (el elctrolumines\$6) and @ad<"20020830" and flexible near4 substrate and (switch\$4 cross\$4 matri\$6) same (connect\$5 interconnect\$5 trace line conduct\$6) and (insulat\$5 isolat\$5) with (connect\$5 interconnect\$5 trace line conduct\$6) and (ic integrated adj circuit chip) and substrate with (line trace connect\$5 interconnect\$5) same power adj suppl\$5
10	substrate and (el elctrolumines\$6) and @ad<"20020830" and flexible near4 substrate and (switch\$4 cross\$4 matri\$6) same (connect\$5 interconnect\$5 trace line conduct\$6) same (insulat\$5 isolat\$5) and (ic integrated adj circuit chip) and substrate with (line trace connect\$5 interconnect\$5) same power adj suppl\$5
11	substrate and (chang\$5 adjust\$5 modif\$6) same path same transmission same (connect\$5 interconnect\$5 trace line conduct\$6) and @ad<"20020830"
12	substrate and (chang\$5 adjust\$5 modif\$6) same path same transmission same (connect\$5 interconnect\$5 trace line conduct\$6) and @ad<"20020830" and (el pixel)
13	substrate and (chang\$5 adjust\$5 modif\$6) same path same transmission same (connect\$5 interconnect\$5 trace line conduct\$6) and @ad<"20020830" and (el pixel) and (surface flexible) near4 substrate
14	substrate and (chang\$5 adjust\$5 modif\$6) same path same transmission same (connect\$5 interconnect\$5 trace line conduct\$6) and @ad<"20020830" and (el pixel) and (flexible) adj substrate
15	substrate and (el elctrolumines\$6) and @ad<"20020830" and flexible near4 substrate jumper same connect\$4 same (disconnect\$ isolat\$4 insulat\$4)
16	substrate and (el electrolumines\$6) and @ad<"20020830" and flexible near4 substrate jumper same connect\$4 same (disconnect\$ isolat\$4 insulat\$4)

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17	substrate and (el electrolumines\$6) and @ad<"20020830" and flexible near4 substrate jumper same connect\$4 same (disconnect\$ isolat\$4 insulat\$4) and substrate with (line trace connect\$5 interconnect\$5) and (ic integrated adj circuit chip)
18	substrate and (el electrolumines\$6) and @ad<"20020830" and flexible near4 substrate jumper same connect\$4 same (disconnect\$ isolat\$4 insulat\$4) and substrate with (line trace connect\$5 interconnect\$5) and (ic integrated adj circuit chip) and (wiring circuit electronic) near2 substrate
19	substrate and (el electrolumines\$6) and @ad<"20020830" and flexible near4 substrate jumper same connect\$4 same (disconnect\$ isolat\$4 insulat\$4) and substrate with (line trace connect\$5 interconnect\$5) and (ic integrated adj circuit chip) and (wiring circuit electronic) near2 substrate and pattern near2 (wiring interconnect\$5 connect\$4)
20	substrate and (el electrolumines\$6) and @ad<"20020830" and flexible near4 substrate jumper same connect\$4 same (disconnect\$ isolat\$4 insulat\$4) and substrate with (line trace connect\$5 interconnect\$5) and (ic integrated adj circuit chip) and (wiring circuit electronic) near2 substrate and pattern near2 (wiring interconnect\$5 connect\$4) and terminal
21	substrate and (el electrolumines\$6) and @ad<"20020830" and flexible near4 substrate jumper same connect\$4 same (disconnect\$ isolat\$4 insulat\$4) and substrate with (line trace connect\$5 interconnect\$5) and (ic integrated adj circuit chip) and (wiring circuit electronic) near2 substrate and pattern near2 (wiring interconnect\$5 connect\$4) and terminal and (chang\$5 modif\$7) with pattern with (line trace connect\$5 interconnect\$5)
22	substrate and (el electrolumines\$6) and @ad<"20020830" and flexible adj substrate jumper same connect\$4 same (disconnect\$ isolat\$4 insulat\$4) and substrate with (line trace connect\$5 interconnect\$5) and (ic integrated adj circuit chip) and (wiring circuit electronic) near2 substrate and pattern near2 (wiring interconnect\$5 connect\$4) and terminal and (chang\$5 modif\$7) with pattern with (line trace connect\$5 interconnect\$5) and surface adj mount\$4
23	substrate and (el electrolumines\$6) and @ad<"20020830" and flexible adj substrate jumper same connect\$4 same (disconnect\$ isolat\$4 insulat\$4) and substrate with (line trace connect\$5 interconnect\$5) and (ic integrated adj circuit chip) and (wiring circuit electronic) near2 substrate and pattern near2 (wiring interconnect\$5 connect\$4) and terminal and (chang\$5 modif\$7) with pattern with (line trace connect\$5 interconnect\$5) and surface adj mount\$4 and pixel
24	substrate and (el electrolumines\$6) and @ad<"20020830" and flexible adj substrate jumper same connect\$4 same (disconnect\$ isolat\$4 insulat\$4) and substrate with (line trace connect\$5 interconnect\$5) and (ic integrated adj circuit chip) and (wiring circuit electronic) near2 substrate and pattern near2 (wiring interconnect\$5 connect\$4) and terminal and (chang\$5 modif\$7) with pattern with (line trace connect\$5 interconnect\$5) and surface adj mount\$4 and pixel and (connect\$5 interconnect\$5) near3 pattern
25	substrate and (el electrolumines\$6) and @ad<"20020830" and flexible adj substrate jumper same connect\$4 same (disconnect\$ isolat\$4 insulat\$4) and substrate with (line trace connect\$5 interconnect\$5) and (ic integrated adj circuit chip) and (wiring circuit electronic) near2 substrate and pattern near2 (wiring interconnect\$5 connect\$4) and terminal and (chang\$5 modif\$7) with pattern with (line trace connect\$5 interconnect\$5) and surface adj mount\$4 and pixel and (connect\$5 interconnect\$5) near6 pattern near6 (chang\$5 modif\$7)

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26	substrate and (el electrolumines\$6) and @ad<"20020830" and flexible adj substrate and jumper same connect\$4 same (disconnect\$ isolat\$4 insulat\$4) and substrate with (line trace connect\$5 interconnect\$5) and (ic integrated adj circuit chip) and (wiring circuit electronic) near2 substrate and pattern near2 (wiring interconnect\$5 connect\$4) and terminal and (chang\$5 modif\$7) with pattern with (line trace connect\$5 interconnect\$5) and surface adj mount\$4 and pixel and (connect\$5 interconnect\$5) near6 pattern near6 (chang\$5 modif\$7)
27	substrate and (el electrolumines\$6) and @ad<"20020830" and flexible adj substrate and substrate with (line trace connect\$5 interconnect\$5) and (ic integrated adj circuit chip) and (wiring circuit electronic) near2 substrate and pattern near2 (wiring interconnect\$5 connect\$4) and terminal and (chang\$5 modif\$7) with pattern with (line trace connect\$5 interconnect\$5) and surface adj mount\$4 and pixel and (connect\$5 interconnect\$5) near6 pattern near6 (chang\$5 modif\$7)
28	@ad<"20020830" and flexible adj substrate and substrate with (line trace connect\$5 interconnect\$5) and (ic integrated adj circuit chip) and (wiring circuit electronic) near2 substrate and pattern near2 (wiring interconnect\$5 connect\$4) and terminal and (chang\$5 modif\$7) with pattern with (line trace connect\$5 interconnect\$5) and surface adj mount\$4 and pixel and (connect\$5 interconnect\$5) near6 pattern near6 (chang\$5 modif\$7)
29	@ad<"20020830" and substrate with (line trace connect\$5 interconnect\$5) and (ic integrated adj circuit chip) and (wiring circuit electronic) near2 substrate and pattern near2 (wiring interconnect\$5 connect\$4) and terminal and (chang\$5 modif\$7) with pattern with (line trace connect\$5 interconnect\$5) and surface adj mount\$4 and pixel and (connect\$5 interconnect\$5) near6 pattern near6 (chang\$5 modif\$7)
30	substrate and @ad<"20020830" and substrate with (line trace connect\$5 interconnect\$5) and (ic integrated adj circuit chip) and (wiring circuit electronic) near2 substrate and pattern near2 (wiring interconnect\$5 connect\$4) and terminal and (chang\$5 modif\$7) with pattern with (line trace connect\$5 interconnect\$5) and surface adj mount\$4 and pixel
31	substrate with (line trace connect\$5 interconnect\$5) and @ad<"20020830" and (ic integrated adj circuit chip) and pattern near2 (wiring interconnect\$5 connect\$4) and terminal and (chang\$5 modif\$7) with pattern with (line trace connect\$5 interconnect\$5) and (el pixel)
32	substrate with (line trace connect\$5 interconnect\$5) and @ad<"20020830" and (ic integrated adj circuit chip) and pattern near2 (wiring interconnect\$5 connect\$4) and terminal and (chang\$5 modif\$7) with pattern with (line trace connect\$5 interconnect\$5) and (el pixel) and flexible near5 substrate
33	substrate and (chang\$5 adjust\$5 modif\$6) same path same transmission same (connect\$5 interconnect\$5 trace line conduct\$6) and @ad<"20020830" and (el pixel) and (flexible) adj substrate and (isolat\$4 disconnect\$4 insulat\$5) with (connect\$5 interconnect\$5 trace line conduct\$6)
34	substrate with (line trace connect\$5 interconnect\$5) and @ad<"20020830" and (ic integrated adj circuit chip) and pattern near2 (wiring interconnect\$5 connect\$4) and terminal and (chang\$5 modif\$7) with pattern with (line trace connect\$5 interconnect\$5) and (el pixel) and flexible near5 substrate and (isolat\$4 disconnect\$4 insulat\$5) with (connect\$5 interconnect\$5 trace line conduct\$6)
35	substrate with (line trace connect\$5 interconnect\$5) and @ad<"20020830" and (ic integrated adj circuit chip) and pattern with (wiring interconnect\$5 connect\$4) with jumper\$5 and terminal and (chang\$5 modif\$7) with pattern with (line trace connect\$5 interconnect\$5) and (el pixel) and flexible near5 substrate

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36	substrate with (line trace connect\$5 interconnect\$5) and @ad<"20020830" and (ic integrated adj circuit chip) and (pattern wiring interconnect\$5 connect\$4) with jumper\$5 and terminal and (chang\$5 modif\$7) with pattern with (line trace connect\$5 interconnect\$5) and (el pixel) and flexible near5 substrate
37	(el pixel) and flexible near5 substrate and substrate with (wir\$5 line trace connect\$5 interconnect\$5) and @ad<"20020830" and (ic integrated adj circuit chip) and (pattern wiring interconnect\$5 connect\$4) with jumper\$5 and terminal and (chang\$5 modif\$7) with pattern with (line trace connect\$5 interconnect\$5)
38	(el pixel) and flexible near5 substrate and substrate with (wir\$5 line trace connect\$5 interconnect\$5) and @ad<"20020830" and (ic integrated adj circuit chip) and (pattern wiring interconnect\$5 connect\$4) with jumper\$5 and terminal